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| | | | | First Named Inventor | Masashi TAKADA |
| | | | | Art Unit | Not Yet Assigned |
| | | | | Examiner Name | Not Yet Assigned |
| Sheet | 1 | of | 1 | Attorney Docket Number | 31869-230501 |

| U.S. PATENT DOCUMENTS | | | | | |
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| Examiner Initials* | Cite No. ¹ | Document Number | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear |
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| DA | JP 2002-326901 | 08/20/2002 | House Ear Institute | | |
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| BC | JP 10-301931 | 02/17/1998 | Philips Electronics N.V. | | |
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| BE | JP 2002094419 A | 03/29/2002 | Toshiba Corp. | | ✓ |
| BF | JP 01198154 A | 08/09/1989 | Oki Electric Ind. Co. Ltd. | | ✓ |

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| | CA | Hideki NOMOTO et al. – “A Study on the Bias Compensation and Variable Step Size Algorithms for the Filtered-X”, Technical Report Of IEICE., DSP 97-14, May 1997, pp. 47-54. | | | |

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| Examiner Signature | /Mohammad Islam/ | Date Considered | 05/15/2010 |
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